

Sheet 1 of 1

USPTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 119118		APPLICATION NO. 10/801,642	
INFORMATION DISCLOSURE STATEMENT  (Use several sheets if necessary)				APPLICANT Masakazu ISOMURA			
				FILING DATE March 17, 2004		GROUP 2183	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
N.M.	1.	JP A 61-211774 w/abstract	9/19/1986	JAPAN			
N.M.	2.	JP A 4-343151 w/abstract	11/30/1992	JAPAN			
N.M.	3.	JP A 1-173165 w/abstract	7/7/1989	JAPAN			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
N.M.	4.	Shigeo Nagashima et al.; "Super Computer"; Ohmsha, Ltd. pp 38 lines 3 to 7 w/partial translation					
EXAMINER				DATE CONSIDERED			
				4/21/06			
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: August 6, 2004

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APPLICANT  
Masakazu ISOMURAFILING DATE  
March 17, 2004GROUP  
~~2183~~ 2187

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
N.M.	1.	JP A 2002-222117 w/abstract & transl.	8/9/2002	JAPAN	—	—
N.M.	2.	JP A 2003-44272 w/abstract & transl.	2/14/2003	JAPAN	—	—
N.M.	3.	JP A 2003-101415 w/abstract & transl.	4/4/2003	JAPAN	—	—

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)


EXAMINER

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4/21/06

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Date: March 7, 2005

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## INFORMATION DISCLOSURE STATEMENT

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APPLICANT  
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March 17, 2004GROUP  
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## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
N.M.	1.	US 3 827 031 A	7/30/1974	KASTNER et al.	712	7

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS
N.M.	2.	EP 0 227 900 A2	7/8/1987	EUROPE		

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

N.M.	3.	J-L. Dekeyser et al.; "Vector addressing processor for direct and indirect accesses"; Microprocessing and Microprogramming Proc. EUROMICRO 90; No. 1/5; August 30, 1990; pp 657-664

EXAMINER

DATE CONSIDERED

4/21/06

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Date: April 13, 2005